



Staff Mixed-Signals Verification Engineer

Are you passionate about new technologies and future-oriented projects in semiconductor design? Photeon is the place for you!

As a market leader in turnkey solutions, Photeon offers the opportunity to work on a diverse range of products across Automotive, Compute, Industrial, Medical, and other markets. We are seeking a dynamic Mixed-Signals Verification Engineer to join our team and drive our projects to success. We provide an energetic, exciting, and friendly environment with numerous opportunities for your professional growth in the direction that suits you best.

We welcome you to become a key contributor to our success, fostering collaboration and teamwork every step of the way.

Your Role:

- Working on Verification of Analog and Mixed-Signal (AMS) products
- Own system-level / full-chip verification
- Define and refine verification concepts, architecture and plans
- Drive model-based verification (RNM, abstraction) and ensure model ↔ schematic consistency
- Architect and oversee development of testbenches and stimuli generation
- Drive integration across Analog and Digital domains (mixed-signal / DMS environments)
- Run simulations, analyze results and lead verification reviews
- Mentor engineers and review verification work

Your Profile:

- Bachelor or Master degree or equivalent in electrical engineering or physics
- ~8+ years experience in analog/mixed-signal verification
- Strong SystemVerilog and advanced modeling (RNM / Verilog-AMS) skills
- Proven experience in system-level verification
- Ability to translate requirements into verification strategies
- Experience with mixed-signal system integration (UVM/DMS beneficial)
- Strong collaboration across multiple domains
- Knowledge of semiconductor physics and semiconductor technologies
- Good verbal and written English skills, German is an advantage

What We Offer:

- Opportunity to work with cutting-edge technology and a talented team
- Personal development and individual training plans
- Innovative products and business environment
- Flexible working hours
- Friendly and supportive environment
- Exciting, varied work in a growing and internationally active company
- Comprehensive onboarding and ongoing support
- Modern office spaces
- Team building events
- Competitive compensation well above the relevant collective agreement (minimum monthly gross salary according to collective agreement "IT" STI Einstiegsstufe EUR 3.267; STI Regelstufe EUR 3.954; ST2 Einstiegsstufe EUR 4.061; ST2 Regelstufe EUR 4.611)



Are you ready to shape the future of semiconductor technology aimed at enabling solutions for a better life? We look forward to receiving your application at recruiting@photeon.com.

Location: Dornbirn, Austria

Contact

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