



NL DELEGATION VISIT

SOITEC 2026

March, 18th 2026



AGENDA

- Section 1 SOITEC GENERAL PRESENTATION
- Section 2 SOITEC FOR PLANET
- Section 3 INNOVATION
- Section 4 PUBLIC AFFAIRS / COLLABORATION PROGRAM
- Section 5 Q&A



Section one

Soitec at a glance

SOITEC IN NUMBERS

IN OUR SOIL GROWS AN AMAZING FUTURE



1992

Soitec is founded

531

Patents filed worldwide each year

4,300

Active patents

17,1%

of revenue dedicated to gross R&D

2200

Employees (35% women)
industry average 20-25%

€891M

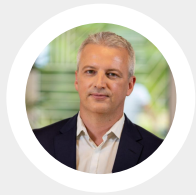
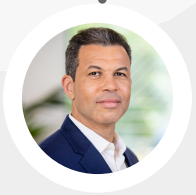
Revenue (Fiscal year 2025)

A VISION SHAPED AND EXECUTED BY OUR GLOBAL MANAGEMENT TEAM



**Chief Executive
Officer**
Pierre Barnabé

Executive Assistant
Florence Stien



**Strategy
Corp. Comm
Marketing**
Steve Babureck

**Human Resources
Transformation
HR Comm**
Jeannette Schuh

**Finance
IR**
Albin Jacquemont

Innovation
Christophe Maleville

**Operations
Excellence Quality**
Cyril Menon

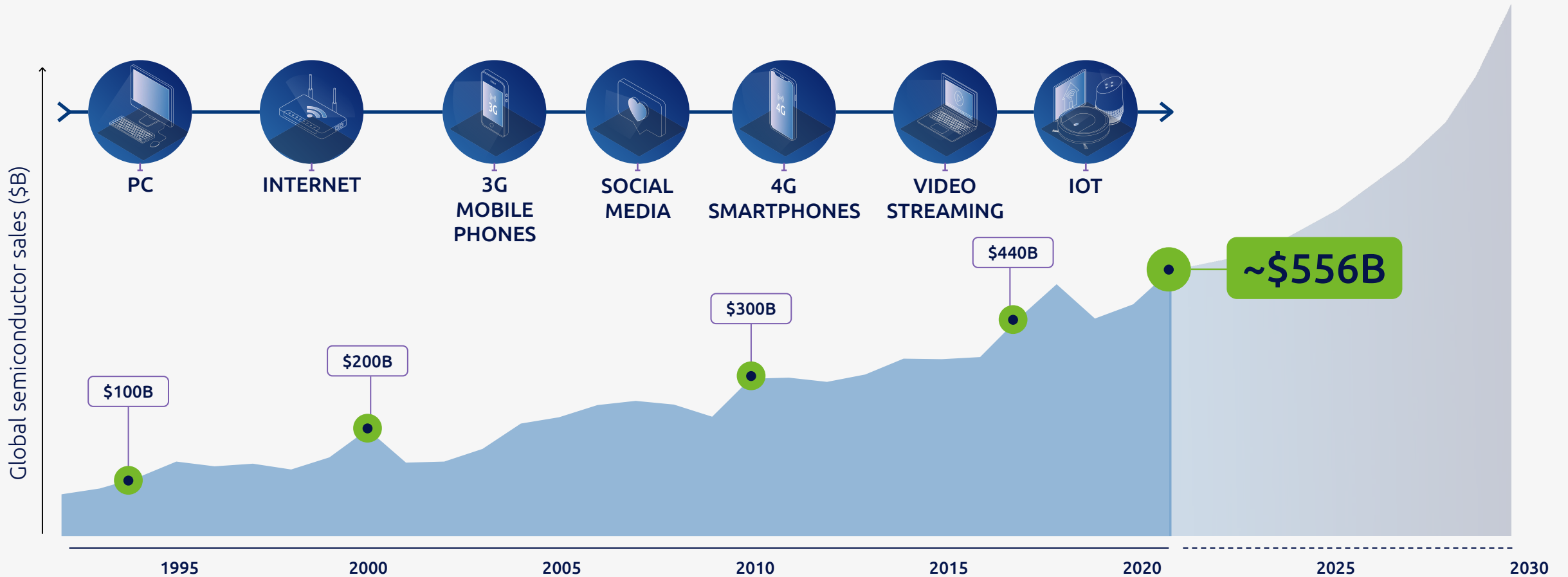
Product Group
René Jonker

General Secretary
Emmanuelle Bely

Sales
Calvin Chen

SEMICONDUCTORS HAVE TRANSFORMED OUR LIVES

FOR THE LAST 30 YEARS

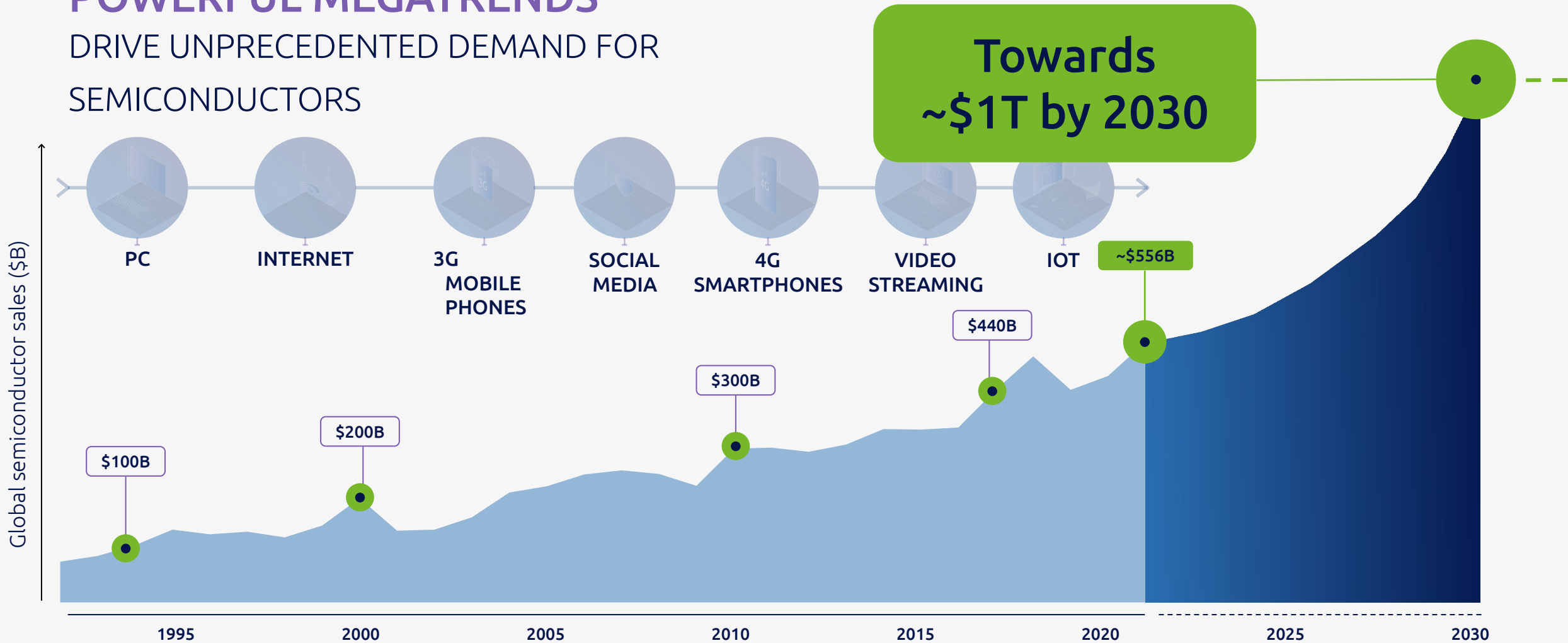


Source: SIA, IBS



POWERFUL MEGATRENDS

DRIVE UNPRECEDENTED DEMAND FOR SEMICONDUCTORS



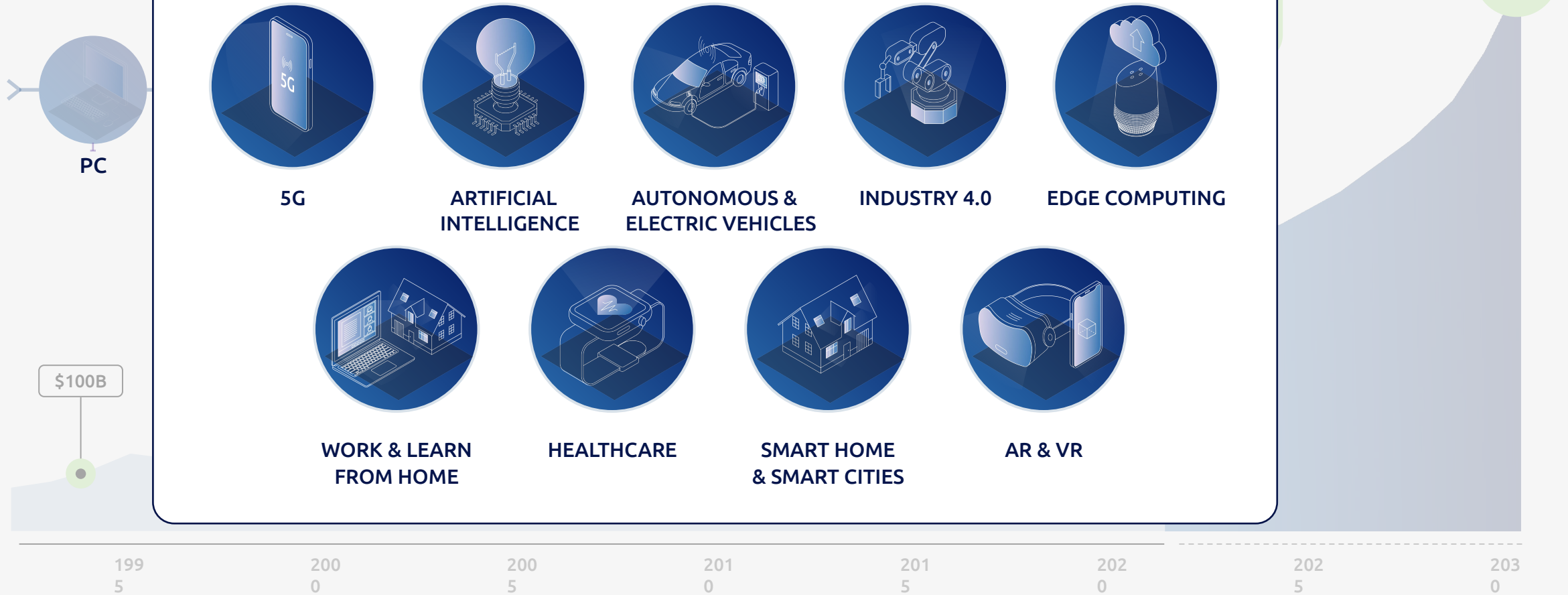
Source: SIA, IBS



POWERFUL MEGATRENDS

DRIVE UP
FOR SEM

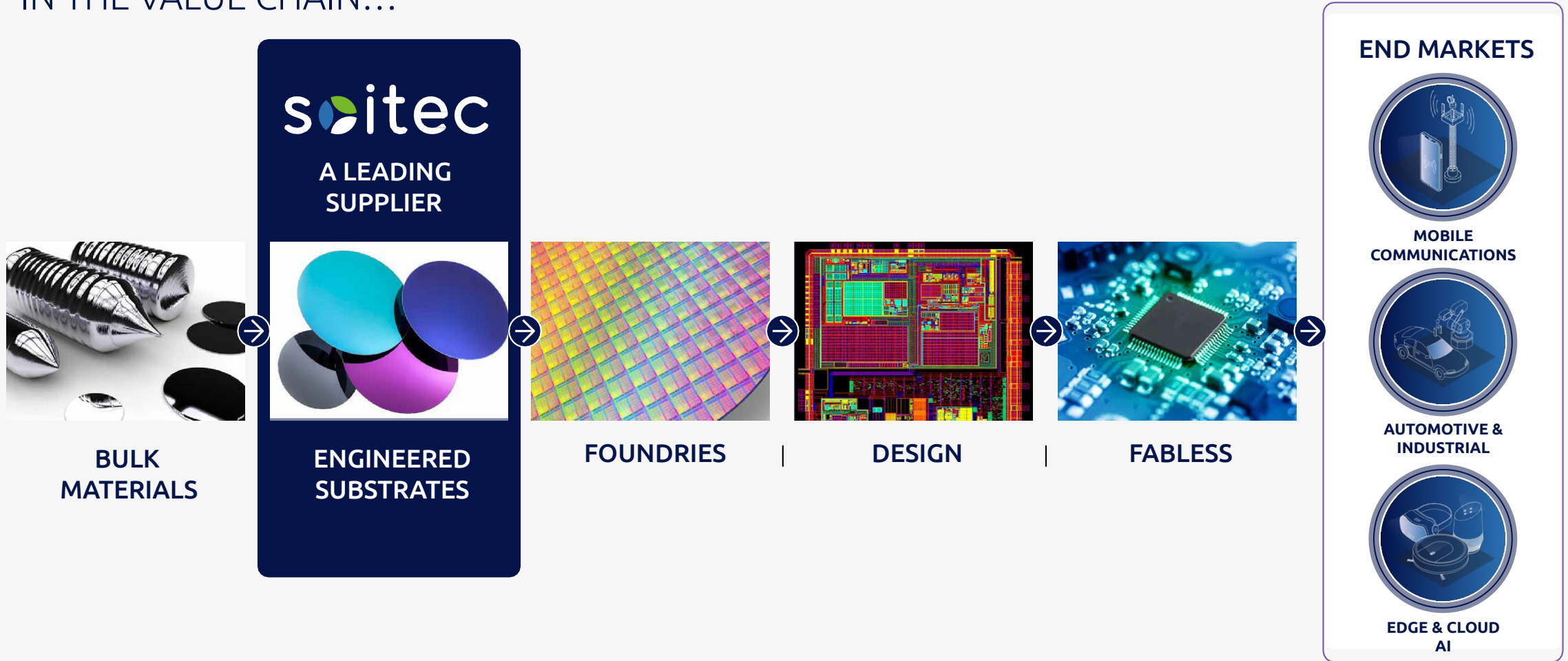
Global semiconductor sales (\$B)



Source: SIA, IBS



SOITEC HAS BUILT A UNIQUE POSITION IN THE VALUE CHAIN...



SOITEC ENGINEERED SUBSTRATES ADDRESS 3 MARKETS

MOBILE COMMUNICATIONS



MAIN DRIVERS

- 5G mmW
- 5G sub-6 GHz
- Mobile infrastructure
- Wi-Fi 7

SOITEC PRODUCTS ENABLE RF SIGNAL INTEGRITY, ENERGY EFFICIENCY, OPTIMIZED FOOTPRINT AND LOWER SYSTEM COST

AUTO & INDUSTRIAL



MAIN DRIVERS

- Autonomous cars
- Vehicle electrification
- Infotainment
- Industry 4.0

SOITEC PRODUCTS ENABLE INCREASED POWER DENSITY, HIGHER SYSTEM EFFICIENCY, RANGE EXTENSION, LOWER SYSTEM COST

EDGE & CLOUD AI



MAIN DRIVERS

- Edge AI computing
- Wearable and Hearable
- Data centers and Cloud AI
- 3D sensing & Healthcare

SOITEC PRODUCTS ENABLE LOWER POWER CONSUMPTION FOR ALWAYS-ON APPLICATIONS AND CLOUD AI HIGH-SPEED OPTICS

SOITEC BUSINESS LINES

Photonics & Optical Sensing



Data center optical I/O



Imaging

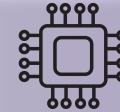


Sensing

FD-SOI



Automotive radar/MCU



Edge Compute/DDIC/ISP



mmWave/ET

RF-SOI



Satcom & D2D



Wi-Fi & FWA



Cellular & Connectivity

POI & Filters Design



5G smartphones

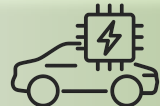


Spatial



Devices geolocation

Power-SOI



Automotive

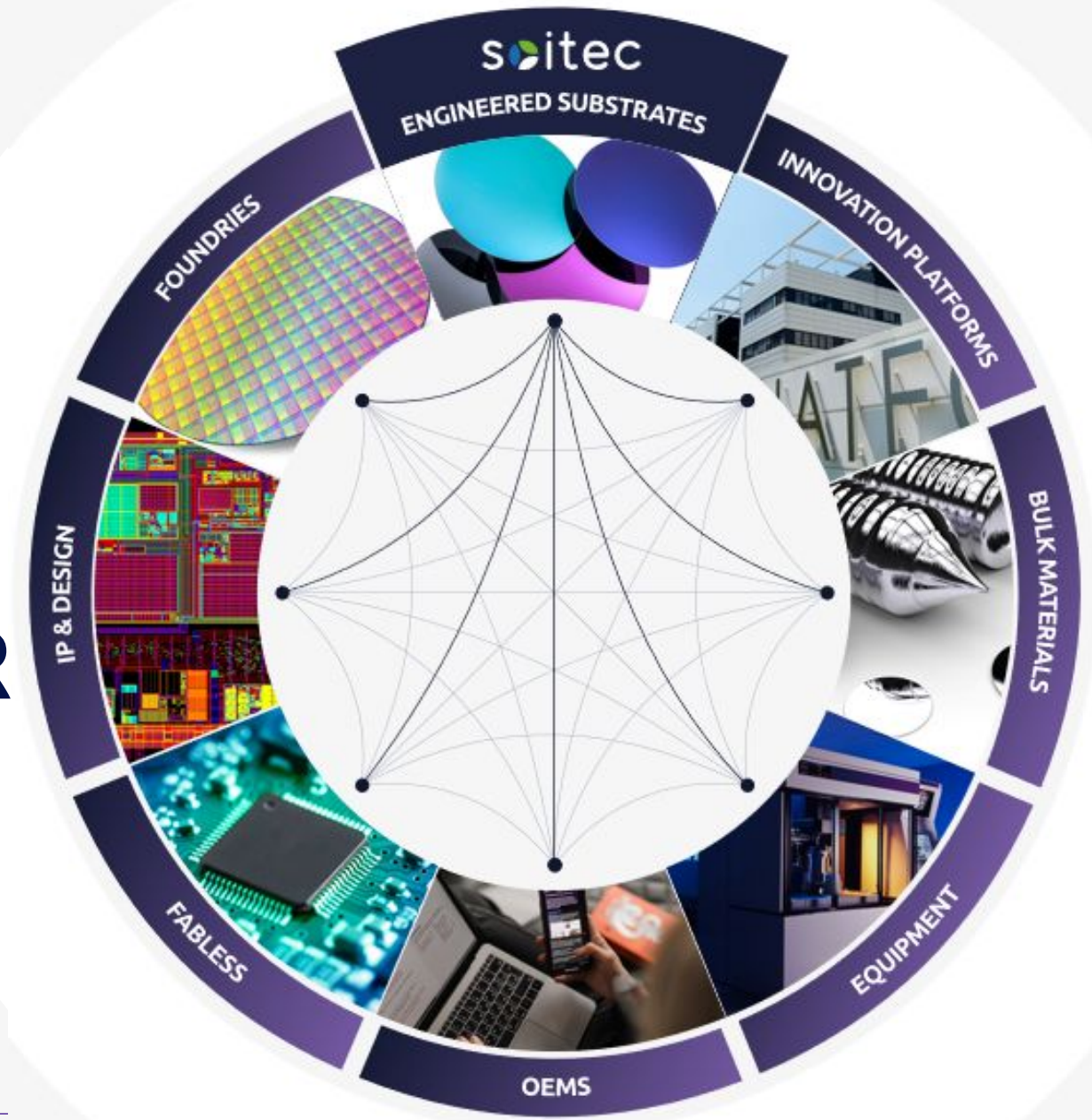


Data center & industrial



Medical markets

... LEVERAGING
STRATEGIC PARTNERSHIPS
IN THE ENTIRE
SEMICONDUCTOR
ECOSYSTEM



GLOBAL PRESENCE & INDUSTRIAL STRENGTH

SOITEC FOOTPRINT



- 150mm
- 200mm
- 300mm

SOITEC BERNIN
FRANCE

B1 : SOI 200mm
IATF 16949

B2 : SOI 300mm
IATF 16949

B3 : POI 150/200mm
LNOI 150mm

B4 : SmartSiC
150/200mm
SOI refresh 300mm

>20 years of high volume manufacturing experience - Site ISO 9001:2015 certified



SIMGUI SHANGHAI (Partner)
CHINA

SOI 200mm

Site IATF 16949 certified

SOITEC BESANÇON
FRANCE

POI SAW filter pilot line

Site ISO 9001:2015 certified



SOITEC BELGIUM N.V
HASSELT
BELGIUM

GaNSi GaNSiC 150/200mm
Site ISO 9001:2015 certified



SOITEC PASIR RIS
SINGAPORE

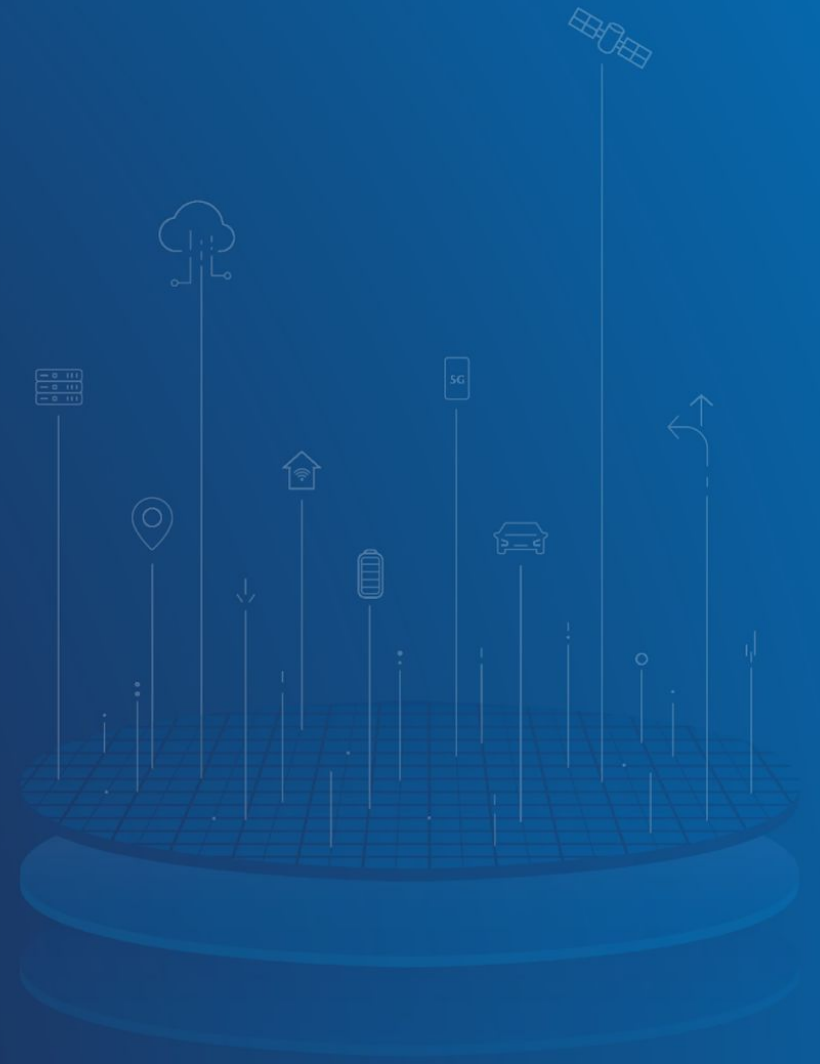
PR1 : SOI 300mm

Site IATF 16949 certified



Section Two

SOITEC FOR THE PLANET





OUR CLIMATE APPROACH

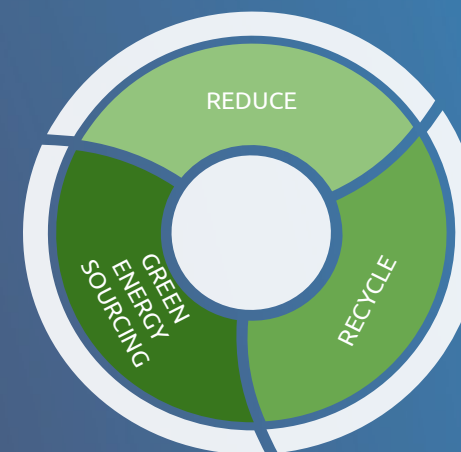
OUR APPROACH

As a player in the microelectronics industry, we must take action to limit the impact of our activities on global warming.

To this end, we have decided to align ourselves with the 2015 Paris Agreement's most ambitious climate pathway: limiting global warming to 1.5°C above pre-industrial levels.



OUR STRATEGY

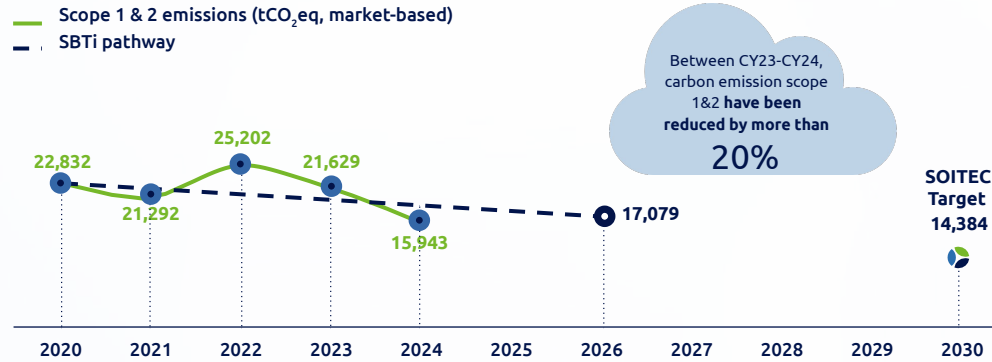


AND NOW?

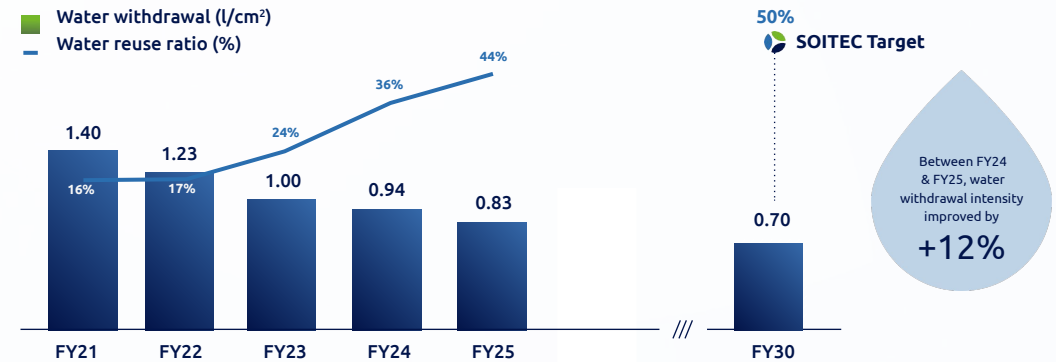
Driving our suppliers decarbonization roadmap and modeling our scope 3 trajectory

DEPLOYMENT OF OUR AMBITIOUS ROADMAP TO SUPPORT SUSTAINABLE GROWTH

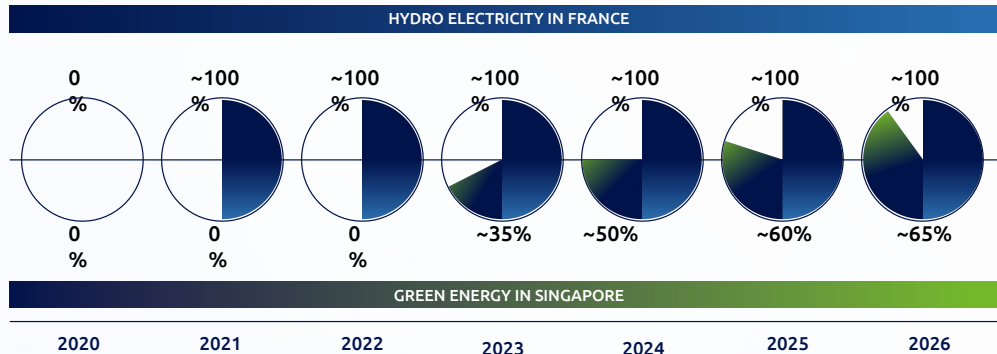
SCOPE 1 & 2 EMISSIONS REDUCTION TARGET REACHED 2 YEARS AHEAD



WATER WITHDRAWAL INTENSITY AND WATER REUSE RATIO



LOW-CARBON ENERGY SOURCING ON TRACK

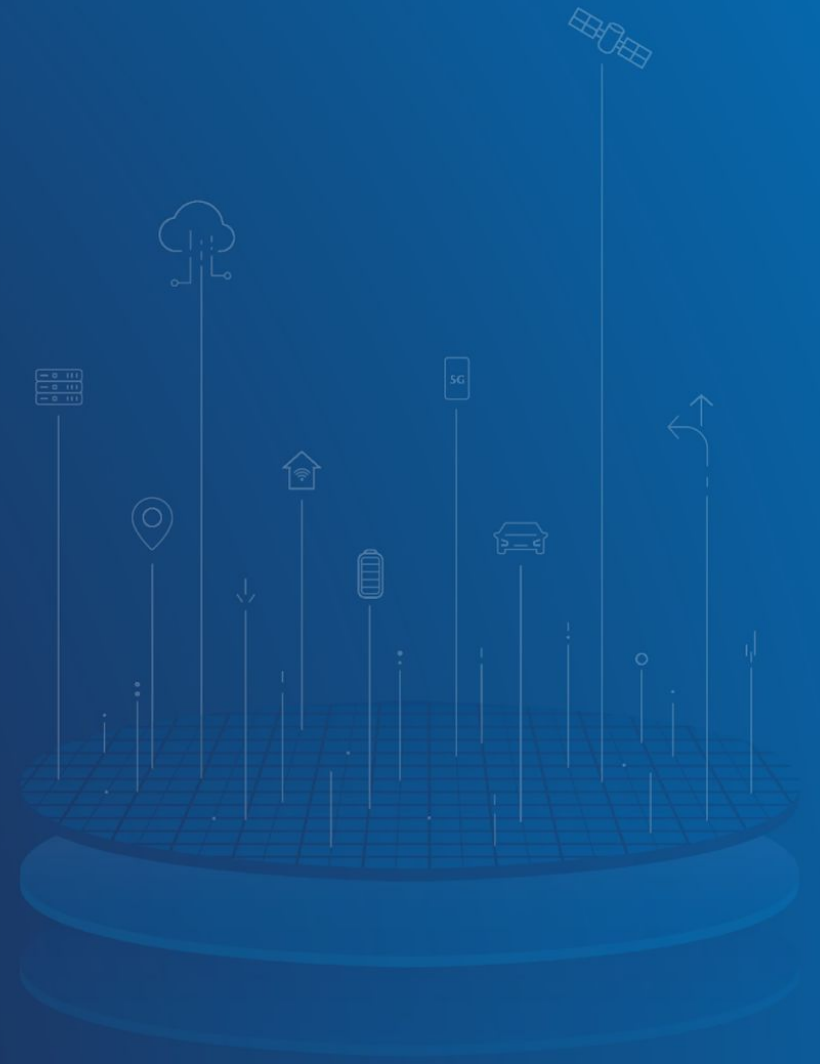


SCOPE 3 KEY ACTIONS HIGHLIGHTS

- Ongoing sourcing roadmap for semiconductor green process gas with N₂ & O₂ already qualified for Bernin site**
- Sea freight qualified customers to reach 50% in FY26**
- >80% of our strategic suppliers sharing and driving carbon reduction roadmap**

Section three

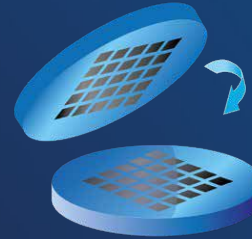
INNOVATION



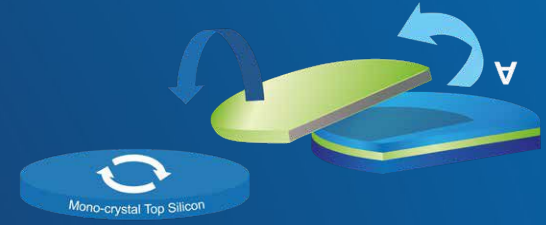
SOITEC CORE TECHNOLOGY TOOLBOX



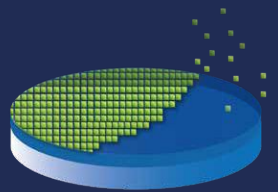
Smart Cut™



Smart Stacking™



Refresh - Repolish



Epitaxy



Material Expertise



Advanced Processing

INNOVATION AT SOITEC IS MULTI-SITE

PARTNERSHIPS WITH LEADING INNOVATION PLATFORMS



The French research center in micro and nanotechnologies



Massachusetts Institute of Technology



Agency for Science, Technology and Research
SINGAPORE



Expanding our R&D depth while keeping compatibility with internal R&D corridors

Early prototyping, focus on lead time and quality

Accessing new ideas, disruptive process improvements, broader expertise

15 worldwide partners



SOITEC LAB @ LETI

OUR SUBSTRATE INNOVATION CENTER



20 researchers (**Soitec Lab**)

30 researchers on development programs & contribution of more than 100 employees (Leti)

>10 external partners



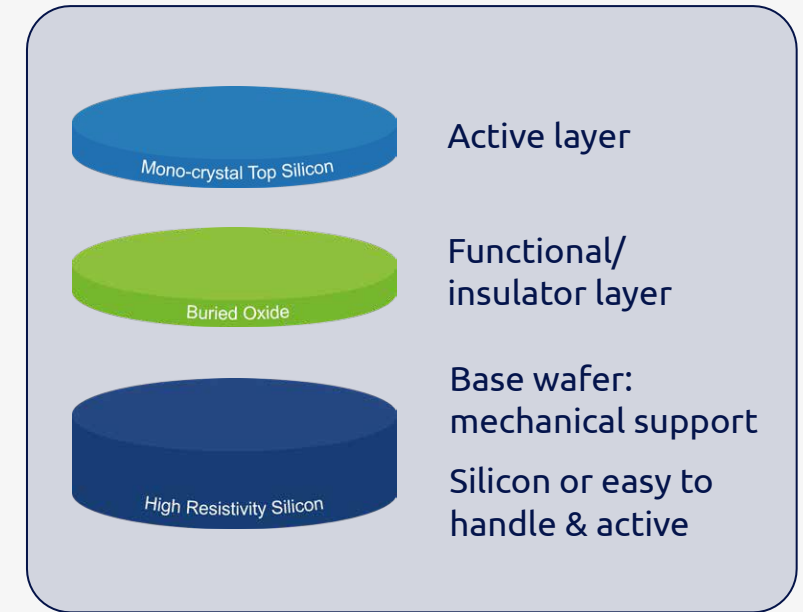
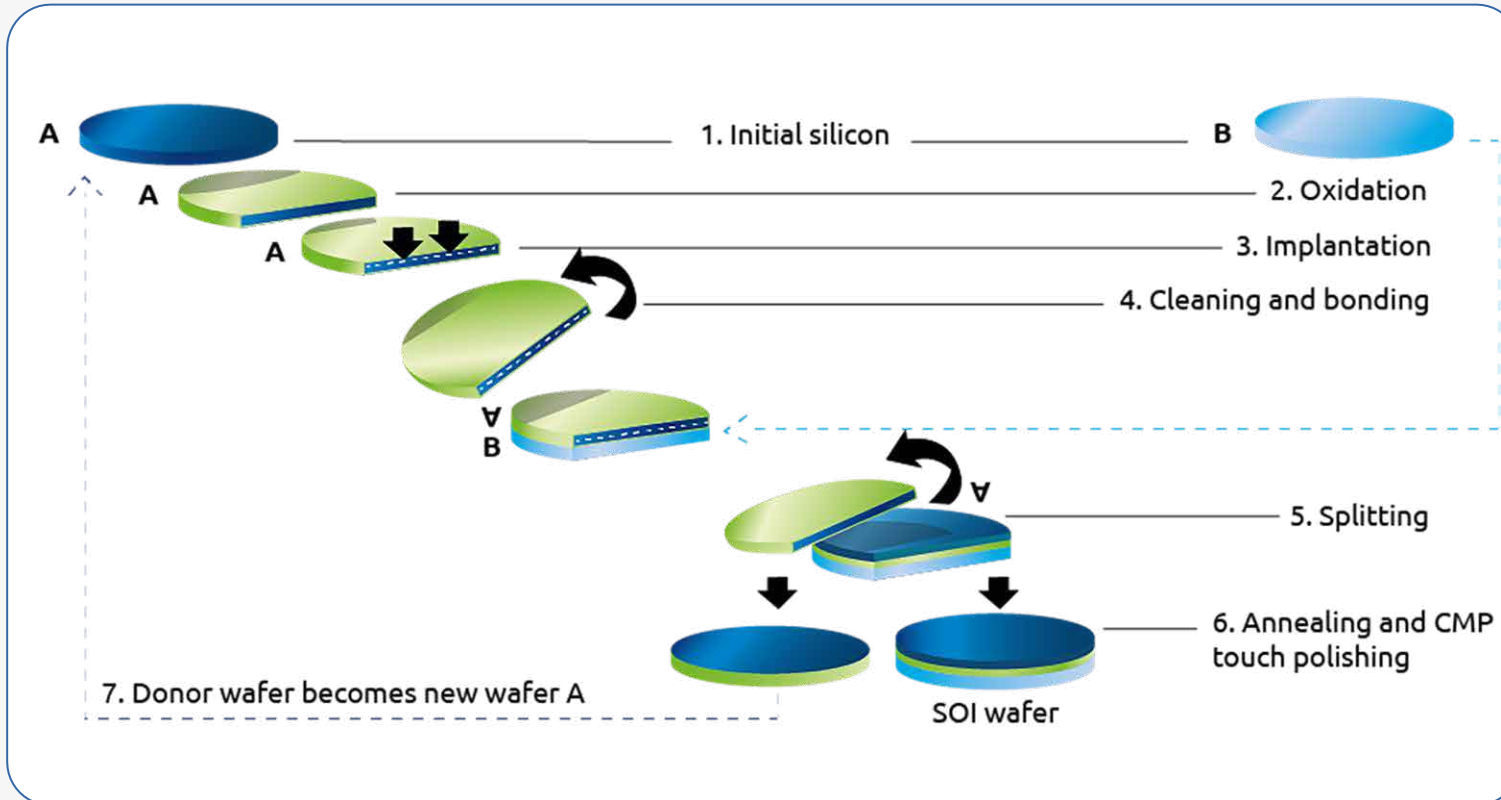
Development
Prototyping



- SmartSiC
- Silicon smoothing
- Epitaxy

- SmartGaN
- InP
- 3D

SMART CUT™ AND SOI SUBSTRATES



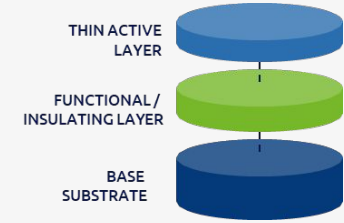
TECHNOLOGY

- Industrial manufacturability of SOI – high yield
- Drastic improvement in uniformity & quality
- Re-use of donor wafer increases cost efficiency
- Flexibility of material integration

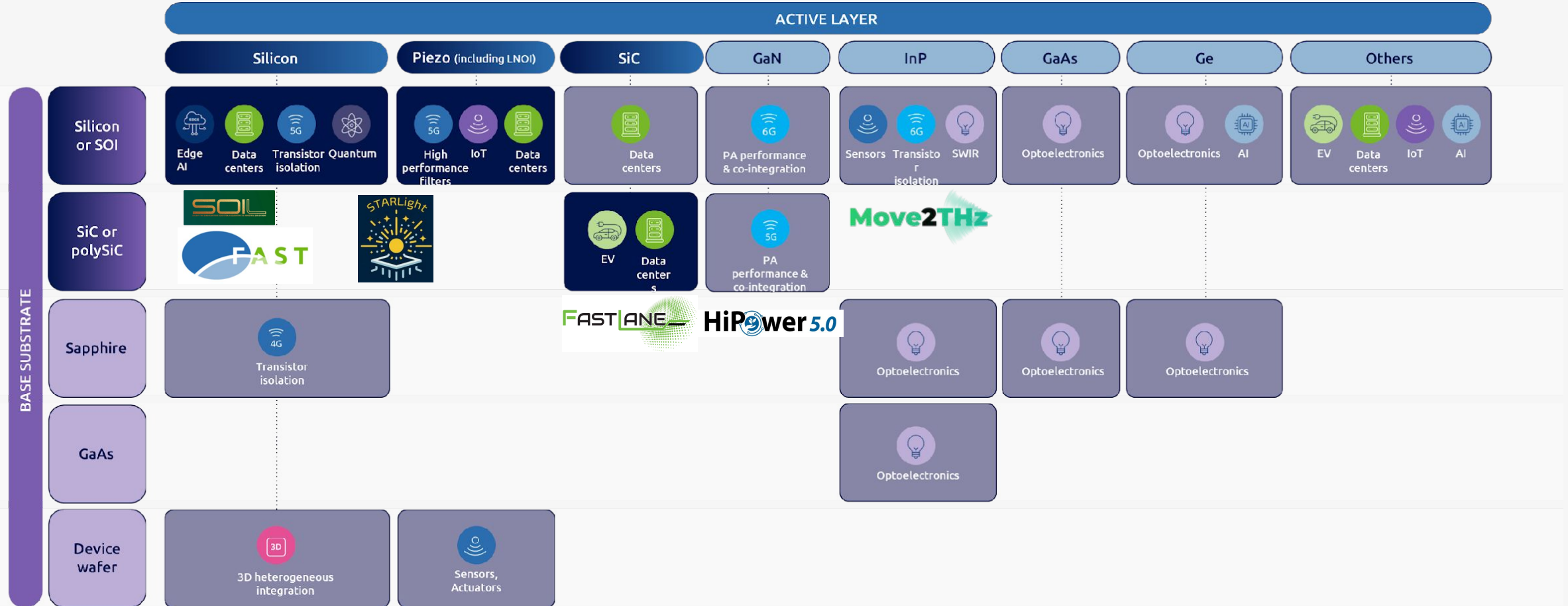
Section Four

Public Affairs Collaborative Programs





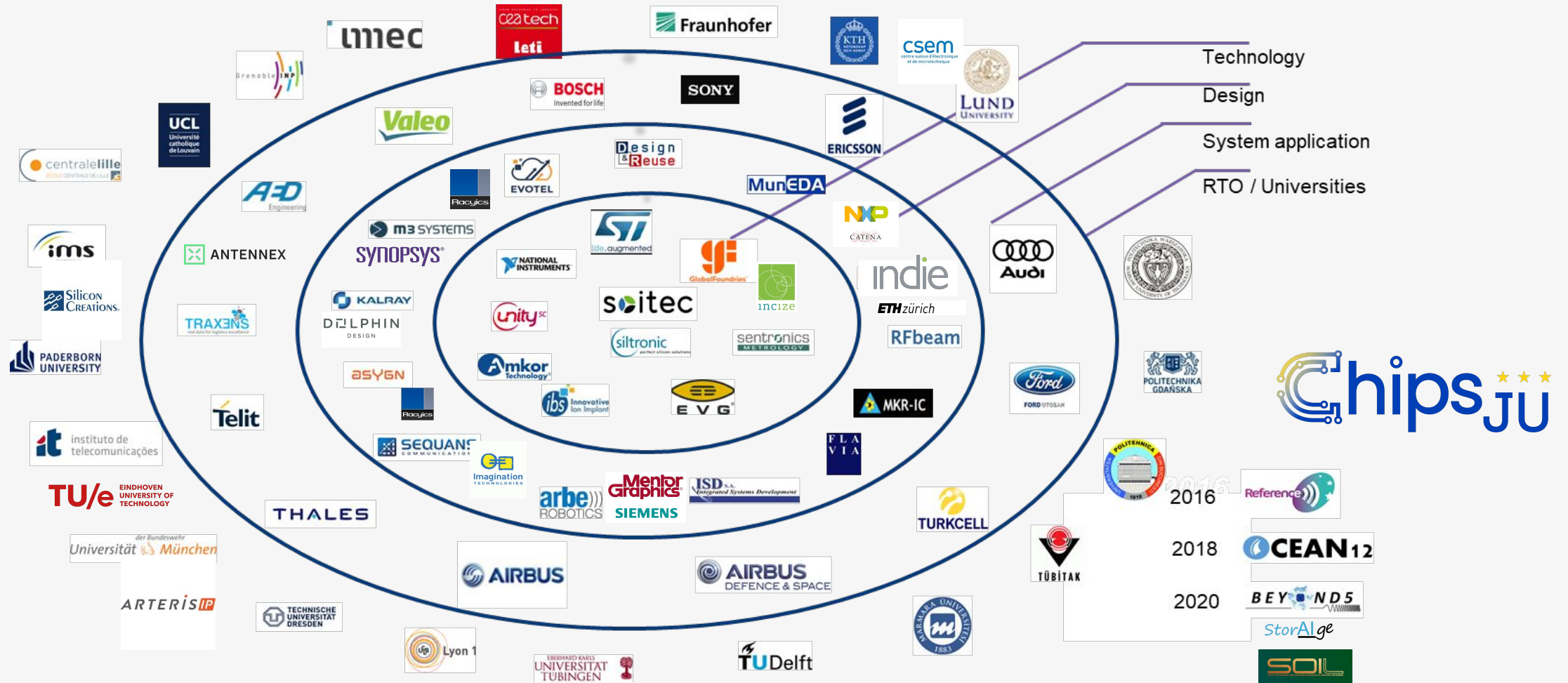
Anything on anything: UNIQUE MATERIALS SCIENCE EXPERTISE TO ENABLE DIFFERENTIATING APPLICATIONS



FD-SOI: a European technology essential for sovereignty

- **Indispensability:** SOI is a standard found in 100% of smartphones and is essential for 5G mmW, 6G, and future generations of Wi-Fi technologies.
- **Resilience:** FD-SOI is a high-performance, low-power technology that is essential for embedded systems in the dual-use, automotive, edge AI, and quantum markets.
- **Prosperity:** Growth is linked to the smartphone, automotive (ADAS), and edge AI markets.

A fully European value chain strengthened project after project



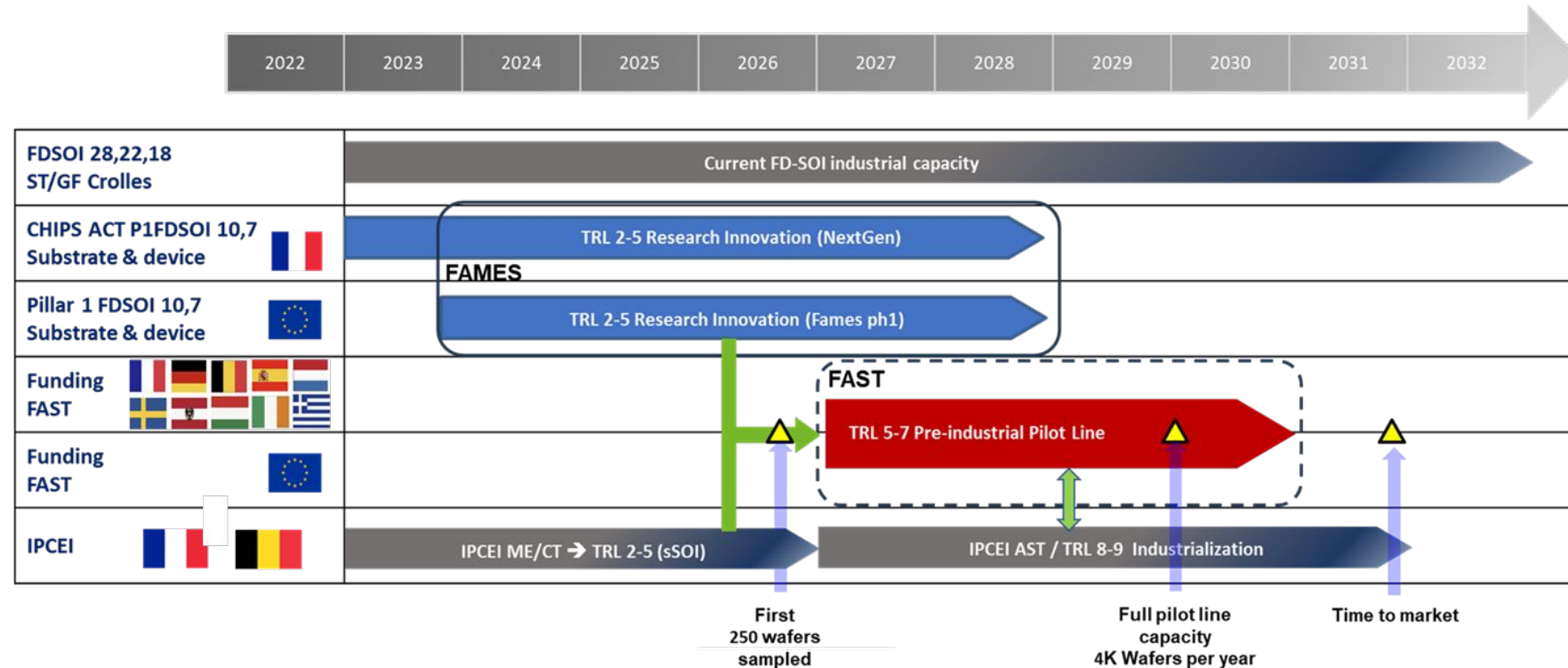
The “FAST” project (Call DEP sSOI) tackles FD-SOI challenges

UNIQUE MATERIALS SCIENCE EXPERTISE TO ENABLE DIFFERENTIATING APPLICATIONS



- **FAST aims to develop the industrial capabilities needed to manufacture the next generation of SOI substrates designed to:**
 - Improve the performance of FD-SOI technology,
 - Enable the continuation of the FD-SOI roadmap towards **nodes below 10nm**
 - Strengthen the European FD-SOI ecosystem by providing a complete supply chain, from materials to design, for the production of high-performance FD-SOI
- **Fast contributes to:**
 - Maintaining European global leadership in SOI
 - Ensuring Europe's independence in its key markets

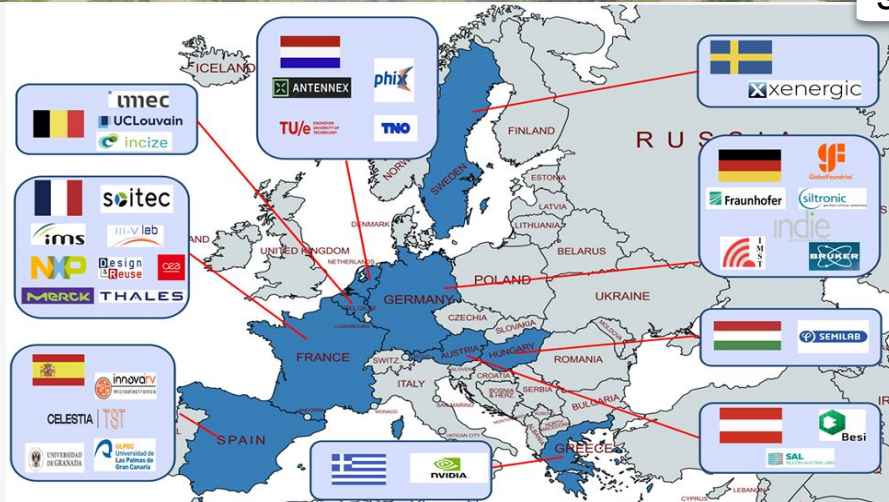
An Industrial sSOI pilot line leveraging FAMES pilot line and driving the next FD-SOI nodes to the market



A Fully European Value Chain based on the Dresden-Grenoble technology



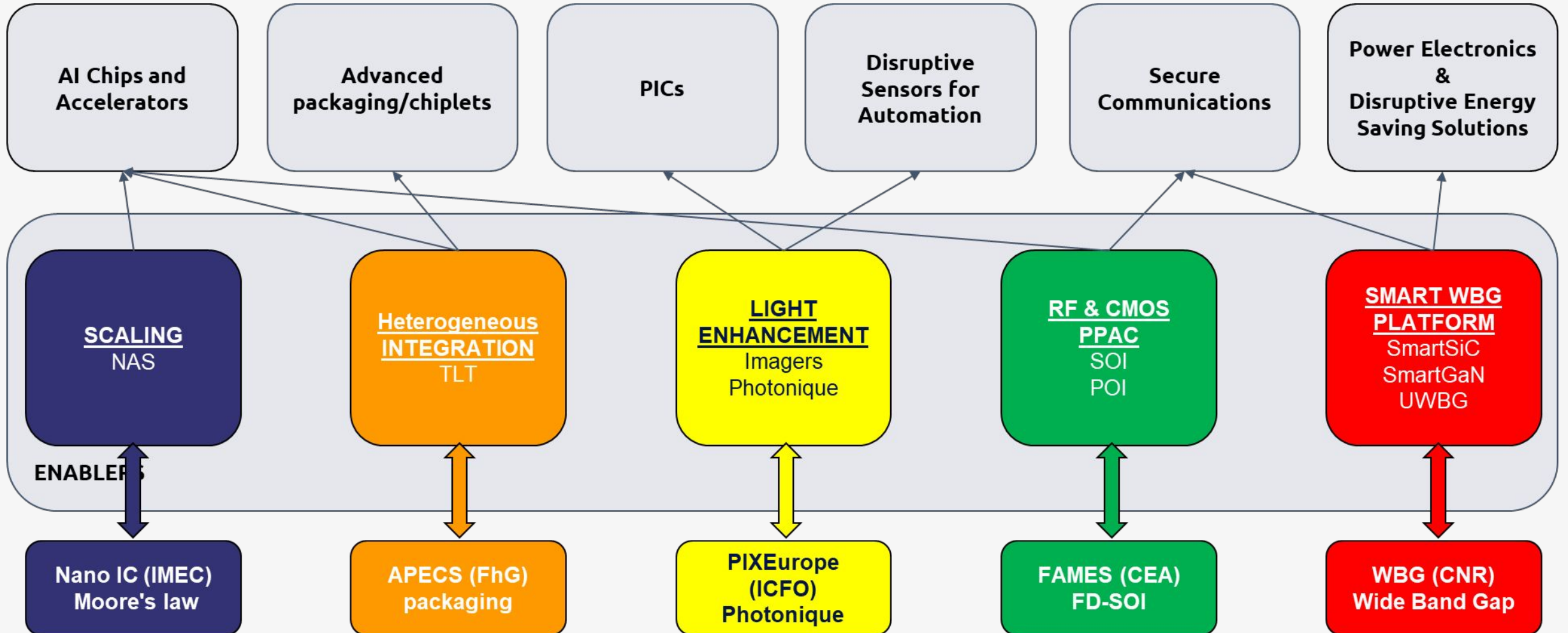
SOITEC, GF, and CEA work closely to qualify the sSOI substrate on GF's 22FDX pilot line in Dresden



The Netherlands:
TU/E,
TNO,
Phix,
Antennex



The new IPCEI AST for SOITEC: enabling the Lab to Fab translation





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THANK YOU

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